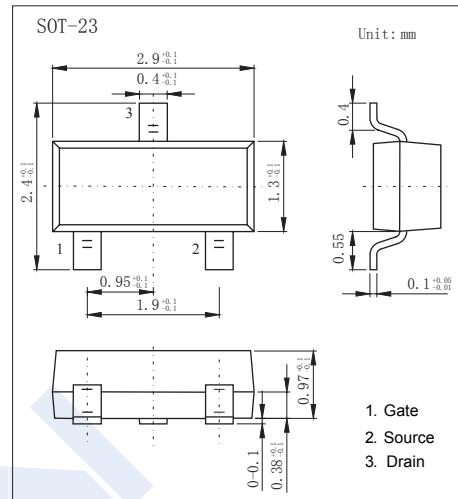
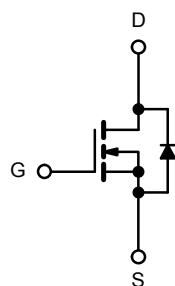


N-Channel MOSFET

SI2318CDS (KI2318CDS)

■ Features

- $V_{DS} (V) = 40V$
- $I_D = 5.6 A$ ($V_{GS} = 10V$)
- $R_{DS(ON)} < 42m\Omega$ ($V_{GS} = 10V$)
- $R_{DS(ON)} < 51m\Omega$ ($V_{GS} = 4.5V$)



■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	40	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current	I_D ($T_c=25^\circ C$)	5.6	A
	I_D ($T_c=70^\circ C$)	4.5	
	I_D ($T_a=25^\circ C$)	4.3	
	I_D ($T_a=70^\circ C$)	3.5	
Pulsed Drain Current	I_{DM}	20	
Power Dissipation	P_D ($T_c=25^\circ C$)	2.1	W
	P_D ($T_c=70^\circ C$)	1.3	
	P_D ($T_a=25^\circ C$)	1.25	
	P_D ($T_a=70^\circ C$)	0.8	
Thermal Resistance.Junction- to-Ambient	R_{thJA}	100	$^\circ C/W$
Thermal Resistance.Junction- to-Foot	R_{thJF}	60	
Junction Temperature	T_J	150	$^\circ C$
Storage Temperature Range	T_{stg}	-55 to 150	

N-Channel MOSFET

SI2318CDS (KI2318CDS)

■ Electrical Characteristics $T_a = 25^\circ\text{C}$

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V_{DSS}	$I_D=250 \mu\text{A}, V_{GS}=0\text{V}$	40			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=40\text{V}, V_{GS}=0\text{V}$			1	μA
		$V_{DS}=40\text{V}, V_{GS}=0\text{V}, T_J=70^\circ\text{C}$			10	
Gate-Body Leakage Current	I_{GSS}	$V_{DS}=0\text{V}, V_{GS}=\pm 20\text{V}$			± 100	nA
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS}=V_{GS}, I_D=250\mu\text{A}$	1.2		2.5	V
Static Drain-Source On-Resistance	$R_{DS(\text{ON})}$	$V_{GS}=10\text{V}, I_D=4.3\text{A}$			42	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}, I_D=3.9\text{A}$			51	
On State Drain Current	$I_{D(\text{ON})}$	$V_{DS} \geq 5\text{V}, V_{GS}=10\text{V}$	20			A
Forward Transconductance	g_{FS}	$V_{DS}=20\text{V}, I_D=4.3\text{A}$		17		S
Input Capacitance	C_{iss}	$V_{GS}=0\text{V}, V_{DS}=20\text{V}, f=1\text{MHz}$		340		pF
Output Capacitance	C_{oss}			60		
Reverse Transfer Capacitance	C_{rss}			30		
Gate Resistance	R_g	$V_{GS}=0\text{V}, V_{DS}=0\text{V}, f=1\text{MHz}$	0.6		6.6	Ω
Total Gate Charge	Q_g	$V_{GS}=20\text{V}, V_{DS}=10\text{V}, I_D=4.3\text{A}$		5.8	9	nC
				2.9	6	
Gate Source Charge	Q_{gs}	$V_{GS}=20\text{V}, V_{DS}=4.5\text{V}, I_D=4.3\text{A}$		1.1		
Gate Drain Charge	Q_{gd}			0.9		
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 20\text{V}, R_L = 5.7\Omega$ $I_D=3.5\text{A}, V_{GEN} = 4.5\text{V}, R_G = 1\Omega$		12	20	ns
Turn-On Rise Time	t_r			50	75	
Turn-Off Delay Time	$t_{d(off)}$			10	20	
Turn-Off Fall Time	t_f			8	16	
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 20\text{V}, R_L = 5.7\Omega$ $I_D=3.5\text{A}, V_{GEN} = 10\text{V}, R_G = 1\Omega$		7	14	ns
Turn-On Rise Time	t_r			20	30	
Turn-Off Delay Time	$t_{d(off)}$			14	21	
Turn-Off Fall Time	t_f			8	16	
Body Diode Reverse Recovery Time	t_{rr}	$I_F=3.5\text{A}, dI/dt=100\text{A}/\mu\text{s}, T_J=25^\circ\text{C}$		15	23	nC
Body Diode Reverse Recovery Charge	Q_{rr}			7	14	
Reverse Recovery Fall Time	t_a			11		
Reverse Recovery Rise Time	t_b			4		
Maximum Body-Diode Continuous Current	I_S	$T_c=25^\circ\text{C}$			1.75	A
Pulse Diode Forward Current	I_{SM}				20	
Diode Forward Voltage	V_{SD}	$I_S=3.5\text{A}, V_{GS}=0\text{V}$			1.2	V

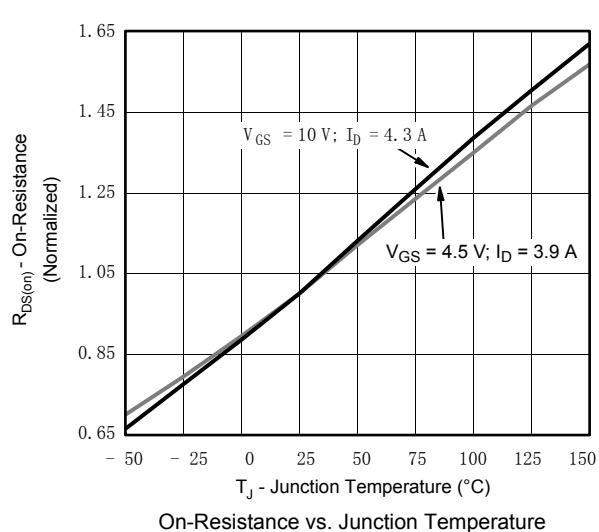
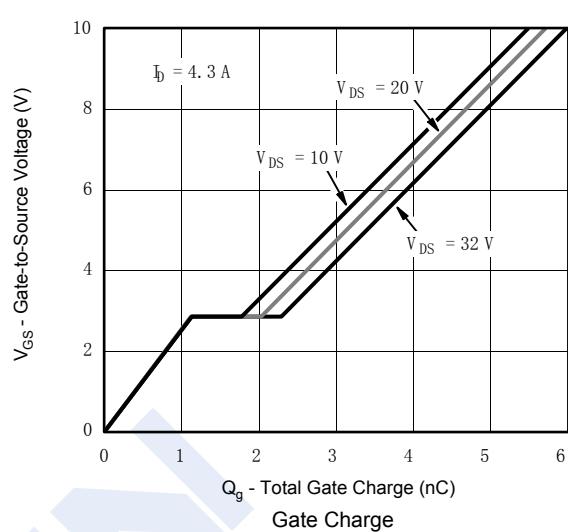
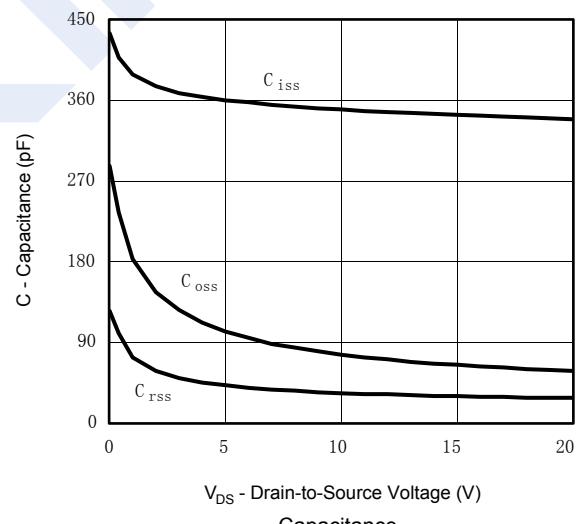
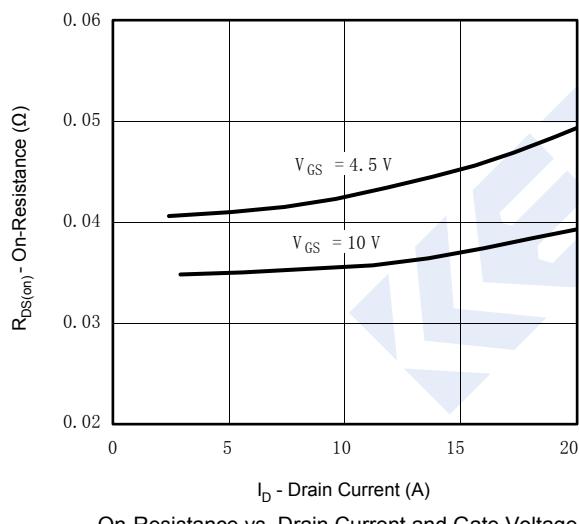
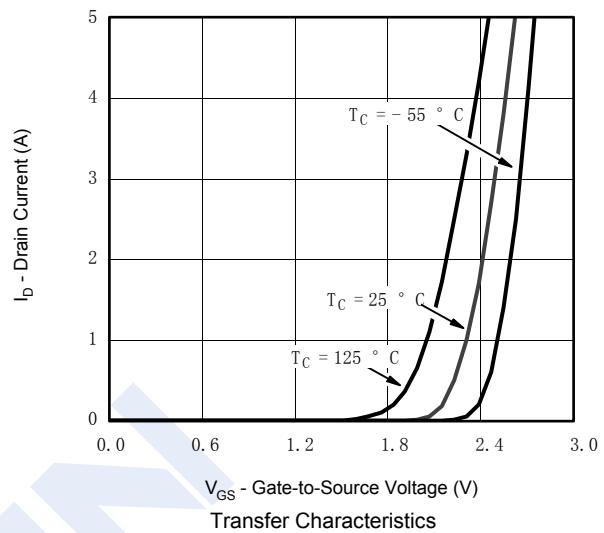
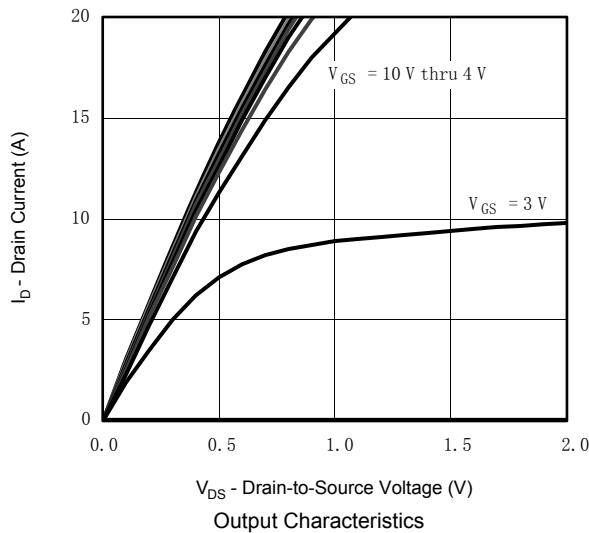
■ Marking

Marking	P9*
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N-Channel MOSFET

SI2318CDS (KI2318CDS)

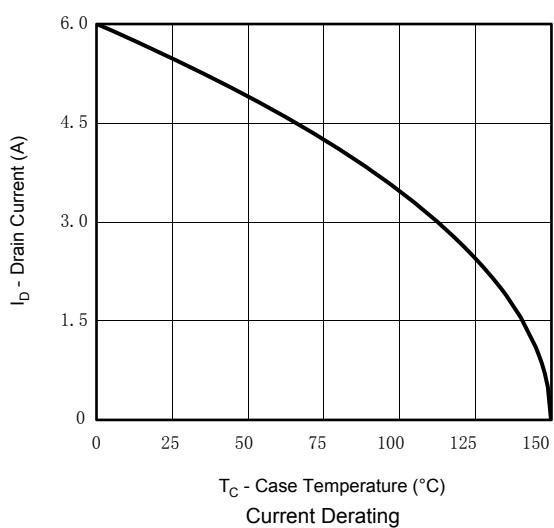
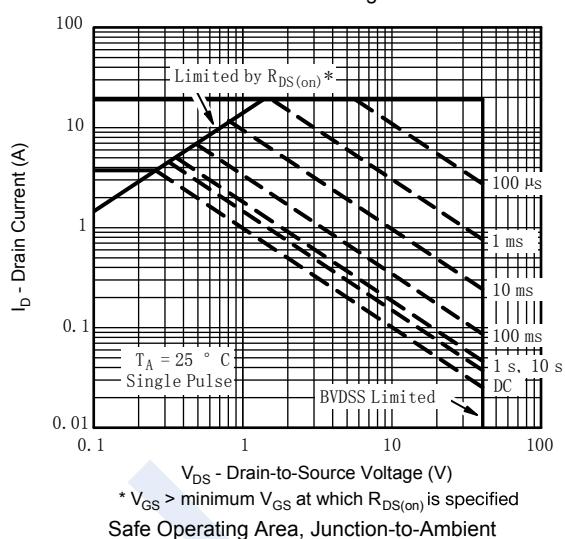
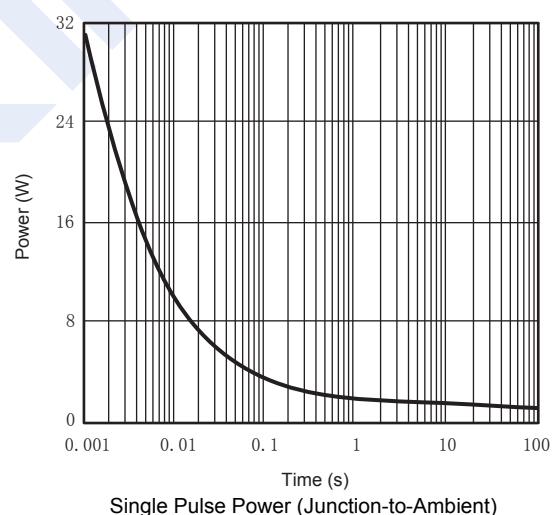
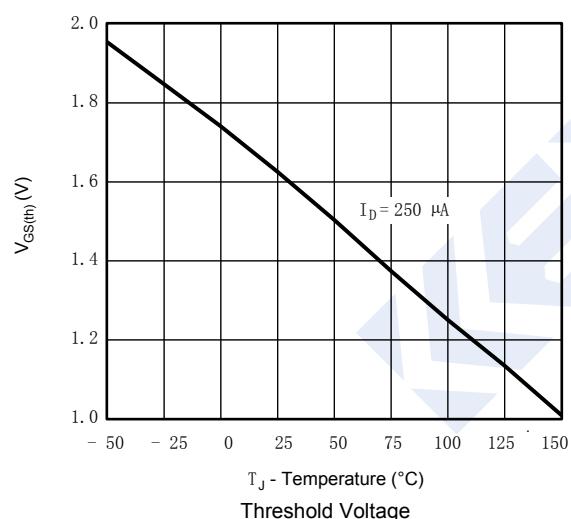
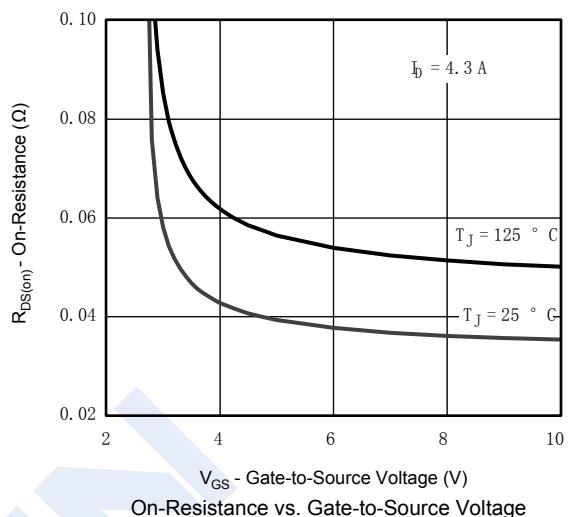
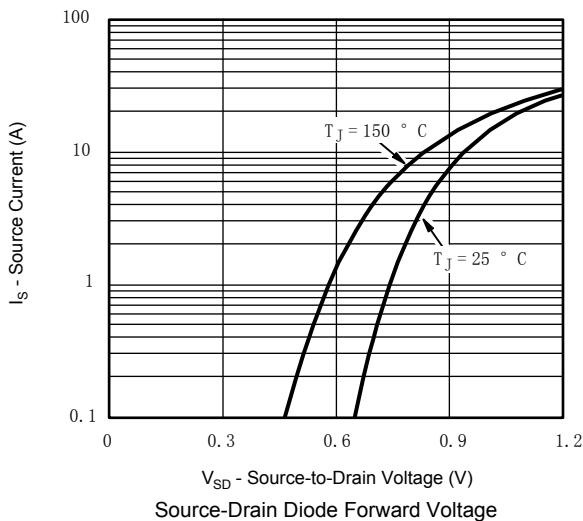
■ Typical Characteristics



N-Channel MOSFET

SI2318CDS (KI2318CDS)

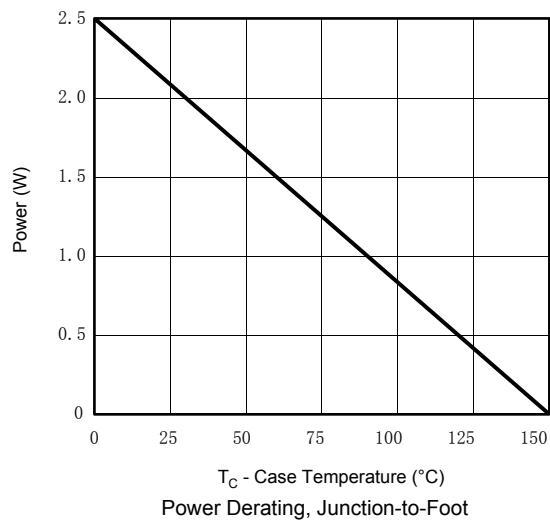
■ Typical Characteristics



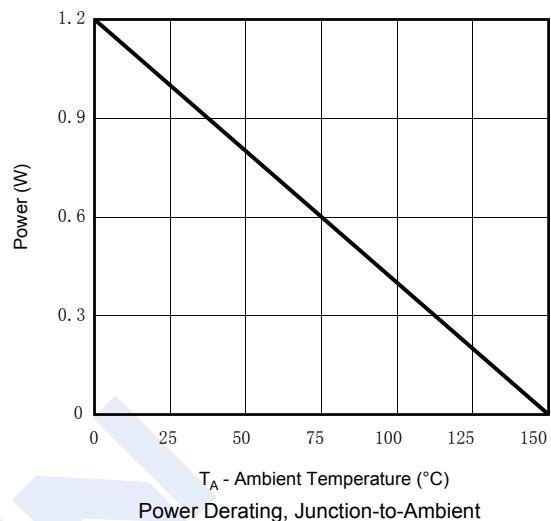
N-Channel MOSFET

SI2318CDS (KI2318CDS)

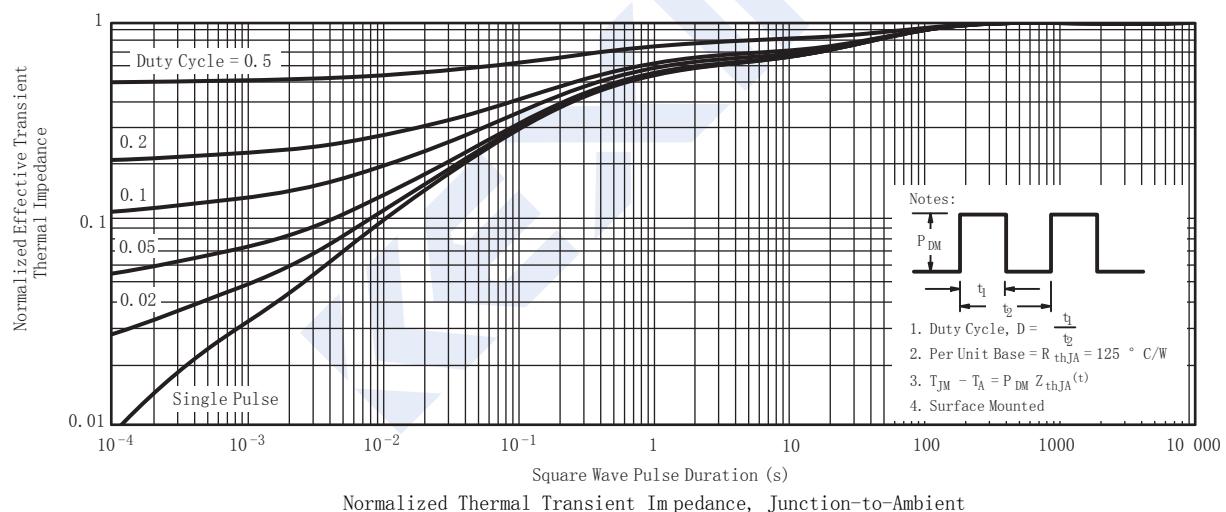
■ Typical Characteristics



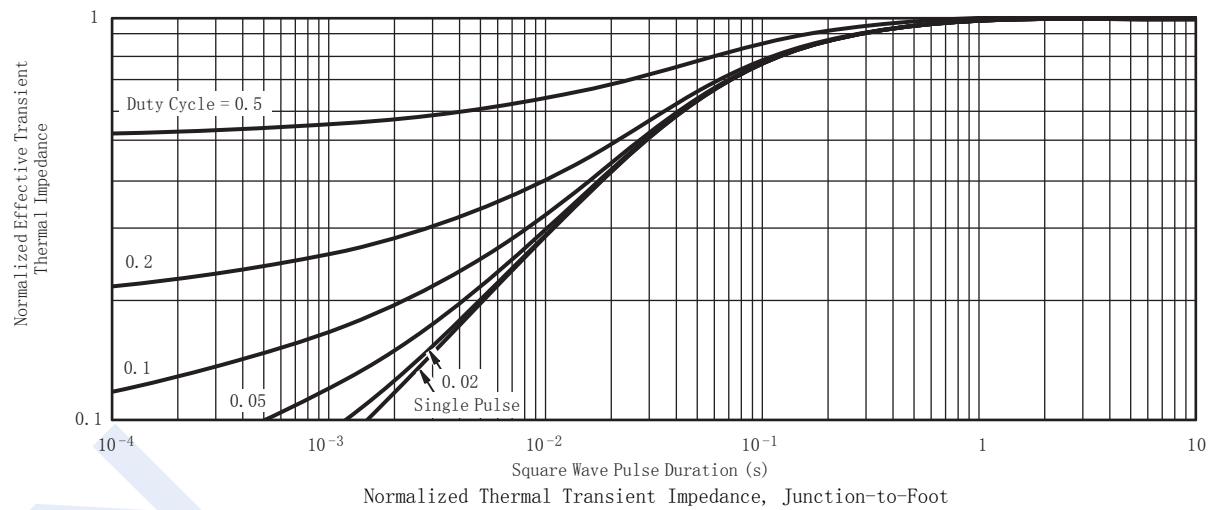
T_c - Case Temperature (°C)
Power Derating, Junction-to-Foot



T_A - Ambient Temperature (°C)
Power Derating, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot